

S/N 10/782,693

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Victor Mercado et al.	Examiner:	Hoi C. Lau
Serial No.:	10/782,693	Group Art Unit:	2636
Filed:	February 19, 2004	Docket:	1842.043US1
Title:	SYSTEM AND METHOD FOR DETECTING TAMPERING IN A GAMING MACHINE		

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants authorize the Commissioner to charge the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p) to Deposit Account 19-0743. Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

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Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any US Patents or Published Applications cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

VICTOR MERCADO ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6909

Date Feb 24, 2006

By Thomas F. Brennan
Thomas F. Brennan
Reg. No. 35,075

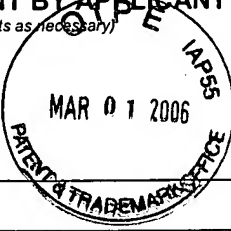
CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS: Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 24th day of February 2006.

Name: THOMAS F. BRENNAN

Signature: Thomas F. Brennan

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Complete if Known	
	Application Number	10/782,693
	Filing Date	February 19, 2004
	First Named Inventor	Mercado, Victor
	Group Art Unit	2636
	Examiner Name	Lau, Hoi
Sheet 1 of 1	Attorney Docket No: 1842.043US1	



US PATENT DOCUMENTS				
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS				
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
		"Basic Stamp BS1-IC Module Schematic Rev. B.", www.parallax.com , (Oct. 5, 2004), 1 page		
		"Basic Stamp FAQ - Frequently Asked Questions and Answer", www.parallax.com/dl/docs/prod/stamps/basicstampfaq.pdf , (July 21, 2000), 1-23		
		"PIC12C5XX - 8-Pin, 8-Bit CMOS Microcontrollers", Microchip Technology , http://www.microchip.com , (1999), 1-113		

EXAMINER

DATE CONSIDERED